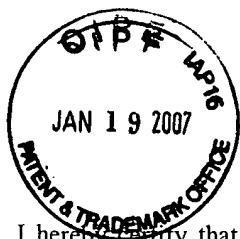


caf



PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date January 12, 2007 Jennifer A. Steele
Jennifer A. Steele

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shane Ching-Feng Hu Attorney Docket No.: 501147.03
Patent No. : US 6,888,732 B2 Serial No. : 10/713,527
Issue Date : May 3, 2005 Filed : November 12, 2003
Title : STATIC CONTENT ADDRESSABLE MEMORY CELL

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate
JAN 23 2007
of Correction

Sir:

A Certificate of Correction under 35 U.S.C. § 254 is respectfully requested for the above-identified patent in order to correct Patent and Trademark Office errors made during the printing of the patent. The changes in the patent needed to correct the errors are as follows:

<u>Column, Line</u>	<u>Reads</u>	<u>Should Read</u>
Item (57), Line 3	"to ground, first and second"	--to ground, and first and second--
Item (57), Line 6	"is coupled"	--are coupled--
Column 1, Lines 25-27	"such as in network-address filtering and translation by matching partial node address. For example, in network router or switch, CAM"	--such as in-network address filtering and translation, by matching partial node addresses. For example, in-network router or switch CAM--
Column 1, Line 60	"to be read sequentially,"	--be read sequentially,--
Column 2, Lines 33-36	"The match circuit discharges a match line in response to a data value stored at the data	--The match circuit discharges a match line in response to a data value stored at the data node to

JAN 23 2007

	node to which the match circuit is coupled and compare data present on the respective data line mismatching."	which the match circuit is coupled and mismatching data present on the respective data line.--
Column 2, Lines 39-41	"The capacitive coupling of the first data node assists in is maintained the charge state."	--The capacitive coupling of the first data node assists in maintaining its charge state.--
Column 4, Lines 25 and 26	"logical level"	--logic level--
Column 4, Line 62	"proceeds as follows."	--proceeds as follows:--
Column 7, Line 31	"under the right data"	--under the write data--

The above errors for which correction is requested under 35 U.S.C. § 254 were made in the printing of the patent or in the original application. The errors are considered sufficiently important to justify the processing of a Certificate of Correction under 35 U.S.C. § 254. A Form PTO-1050, in duplicate, is enclosed herewith.

The Commissioner is hereby authorized to charge payment of any fees associated with this communication to Deposit Account No. 50-1266. A duplicate copy of this sheet is enclosed. Favorable consideration of this Request is respectfully requested.

Respectfully submitted,

Date:

Jan. 11, 2007

By:

Edward W. Bulchis

Edward W. Bulchis, Reg. No. 26,847

Customer No. 27,076

Dorsey & Whitney LLP

1420 Fifth Avenue, Suite 3400

Seattle, WA 98101

(206) 903-8785

Attorney for Applicant(s)

EWB:tdp

Enclosures:

Postcard

Form PTO-1050 (+ copy)

JAN 23 2007



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Attorney for Applicant(s)

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**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO. : US 6,888,732 B2
DATED : May 3, 2005
INVENTOR(S) : Shane Ching-Feng Hu

It is certified that errors appear in the above identified patent and that said Letters Patent is hereby corrected as shown below:

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JAN 23 2007

Column 7, Line 31

"under the right data"

--under the write data--

MAILING ADDRESS OF SENDER:

DORSEY & WHITNEY LLP
1420 Fifth Avenue, Suite 3400
Seattle, Washington 98101

Patent No. 6,888,732 B2

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JAN 23 2007

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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